

02527 U.S. PTO
08/19/03

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

ATTY DOCKET NO.: 5646-81

DATE: August 18, 2003

**UTILITY PATENT APPLICATION TRANSMITTAL LETTER
AND FEE TRANSMITTAL FORM (37 CFR § 1.53(b))**

Mail Stop PATENT APPLICATION
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

16235 U.S. PTO
10/643208
08/19/03

Sir:

Transmitted herewith for filing under 37 CFR § 1.53(b) is:

- ☒ a patent application
☐ a Continuation ☐ a Divisional ☐ a Continuation-in-Part (CIP) Application of prior application no.: _____; filed _____, the disclosure of which is incorporated herein by reference in its entirety.
☒ and claims the benefit of Provisional Application No. 60/412,514; filed September 20, 2002.

Inventor(s) or Application Identifier:

Robert J. Proebsting – Senora, California
Cesar A. Talledo – San Jose, California
David J. Pilling – Los Altos Hills, California

Entitled: **INTEGRATED CIRCUIT DEVICES HAVING HIGH PRECISION DIGITAL
DELAY LINES THEREIN**

Myers Bigel Sibley & Sajovec, P.A.
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Raleigh, North Carolina 27627
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Enclosed are:

1. ☒ Application Transmittal Letter and Fee Transmittal Form (*A duplicate is enclosed for fee processing*)
2. ☒ 70 pages of Specification (including 134 claims)
3. ☒ 19 sheets of Formal Drawings (35 USC 113)
4. ☐ Oath or Declaration
 - a. ☐ newly executed (*original or copy*)
 - b. ☐ copy from prior application (37 CFR 1.63(d) (*for continuation/divisional*) [Note Box 6 Below]
 - c. ☐ DELETION OF INVENTOR(S) (*Signed statement deleting inventor(s) named in the prior application*)
5. ☐ Application Data Sheet. See 37 CFR 1.76
6. ☐ Incorporation By Reference (*useable if box 4b is checked*)

The entire disclosure of the prior application, from which a copy of the oath or declaration is supplied under Box 4b, is considered as being part of the disclosure of the accompanying application and is hereby incorporated by reference therein.

7. ☐ Microfiche Computer Program (*Appendix*)
8. ☐ Assignment papers (*cover sheet(s) and document(s)*)
9. ☐ Applicant claims small entity status. See 37 CFR § 1.27.
10. ☒ Information Disclosure Statement, PTO-1449, and _____ references cited
11. ☐ Preliminary Amendment (*Please enter all claim amendments prior to calculating the filing fee.*)
12. ☐ English Translation Document
13. ☐ Certified Copy of _____ Application No. _____ ; Filed
14. ☐ Sequence Listing/ Sequence Listing Diskette
 - a. ☐ computer readable copy
 - b. ☐ paper copy
 - c. ☐ statement in support
15. ☐ An Associate Power of Attorney
16. ☒ Return Receipt Postcard (MPEP 503) (*Should be specifically itemized*)
17. ☒ Other: Application Filed Under 37 CFR 1.41(c); Nonpublication Request

The fee has been calculated as shown below:

	Column 1 No. Filed	Column 2 No. Extra	Small Entity Rate Fee	Large Entity Rate Fee
BASIC FEE			\$375.00	\$ 750.00
TOTAL CLAIMS	18 - 20 =	0	x 09 = \$	x 18 = \$ 0
INDEP CLAIMS	3 - 3 =	0	x 42 = \$	x 84 = \$ 0
<input type="checkbox"/> MULTIPLE Dependent Claims Presented			+ 140 = \$	+ 280 = \$
<i>If the difference in Col. 1 is less than zero, Enter "0" in Col. 2</i>			Total \$	Total \$750.00

- ☐ A check in the amount of \$ _____ to cover the filing fee is enclosed.
- ☐ A check in the amount of \$ _____ is enclosed to cover the filing fee, PLUS the Assignment Recordation fee (\$40.00).
- ☐ Please charge my Deposit Account No. _____ in the amount of \$ _____.
- ☐ The Commissioner is hereby authorized to credit overpayments or charge the following fees associated with this communication to Deposit Account No. _____:
- a. ☐ Additional filing fees under 37 CFR 1.16 for presentation of extra claims.
 - b. ☐ Additional patent application processing fees under 37 CFR 1.17.

Respectfully submitted,


Grant J. Scott
Registration No. 36,925

CERTIFICATE OF EXPRESS MAILING

Express Mail Label No. EV 318419992 US

Date of Deposit: August 18, 2003

I hereby certify that this correspondence is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR § 1.10 on the date indicated above and is addressed to: Mail Stop PATENT APPLICATION, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450


Candi L. Riggs

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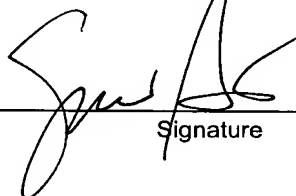
NONPUBLICATION REQUEST UNDER 35 U.S.C. 122(b)(2)(B)(i)	First Named Inventor	Robert J. Pr ebsting
	Title	Integrated Circuit Devices Having High Precision Digital Delay Lines Therein
	Attorney Docket Number	5646-81

I hereby certify that the invention disclosed in the attached application **has not and will not be** the subject of an application filed in another country, or under a multilateral agreement, that requires publication at eighteen months after filing.

I hereby request that the attached application not be published under 35 U.S.C. 122(b).

August 18, 2003

Date



Signature

919/854-1400

Telephone number

Grant J. Scott #36,925

Typed or printed name

This request must be signed in compliance with 37 CFR 1.33(b) and submitted with the application **upon filing**.

Applicant may rescind this nonpublication request at any time. If applicant rescinds a request that an application not be published under 35 U.S.C. 122(b), the application will be scheduled for publication at eighteen months from the earliest claimed filing date for which a benefit is claimed.

If applicant subsequently files an application directed to the invention disclosed in the attached application in another country, or under a multilateral international agreement, that requires publication of applications eighteen months after filing, the applicant **must** notify the United States Patent and Trademark Office of such filing within forty-five (45) days after the date of the filing of such foreign or international application. **Failure to do so will result in abandonment of this application (35 U.S.C. 122(b)(2)(B)(iii)).**

This collection of information is required by 37 CFR 1.213(a). The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 6 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

If you need assistance in completing the form, call 1-800-PTO-9199 (1-800-786-9199) and select option 2.

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re: Robert J. Proebsting et al.

Serial No. To be assigned

Filed: Concurrently herewith

For: INTEGRATED CIRCUIT DEVICES HAVING HIGH PRECISION DIGITAL
DELAY LINES THEREIN

Date: August 18, 2003

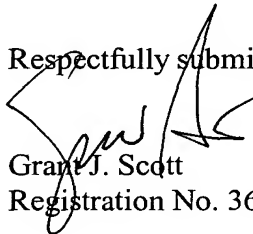
Mail Box Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

APPLICATION FILED UNDER 37 CFR 1.41(c)

Sir:

The above-identified application is being filed on behalf of the inventors,
Robert J. Proebsting, Cesar A. Talledo and David J. Pilling, residents of California, under the
provisions of 37 CFR 1.41(c). A Declaration and Power of Attorney from the inventors will
follow, 37 CFR 1.63.

Respectfully submitted,


Grant J. Scott
Registration No. 36,925

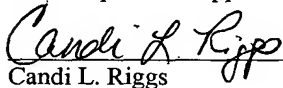
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Candi L. Riggs